



डिपार्टमेंट ऑफ इलेक्ट्रॉनिक्स इंजीनियरिंग

DEPARTMENT OF ELECTRONICS ENGINEERING

सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ टेक्नोलॉजी, सूरत

SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT

क्रमांक: डीईसीई/ एम.टेक (रिसर्च)/ प्रवेश / 731 / २०२५-२६

दिनांक: ०९/०७/२०२५

M. Tech. (Research) Admissions June-2025

Autumn Semester (A. Y.: 2025-26)

Selected Candidate


The following candidates are recommended / not recommended for admission to M. Tech. (Research) offered by the Department of Electronics Engineering.

Sr. No.	Application No.	Name of Candidates	Branch	Supervisor / Co-supervisor	Remark
3.	SVNIT7MUWS	NIKET SINGLA	VLSI & Embedded Systems	Dr. P. J. Engineer	--
4.	SVNIT7MJD2	VAIBHAV ACHARYA	VLSI & Embedded Systems	Dr. Abhishek Acharya Dr. Deepak Joshi	Original sponsorship certificate/no objection certificate will be submitted at the time of Registration

NOTES:

- All selected candidates shall complete the registration formalities and complete the Fee Payment between 7th July to 16th July 2025.
- The admissions are governed by the academic rules and regulations of the programme.


Dr. Shilpi Gupta
Head, DoECE


Dean (Academic)